

10/029778  
10/18/01

PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10029778	FILING DATE 10/18/2001	CLASS 374	SUBCLASS 030	GAU 2841	EXAMINER ALCATA trnh
----------------------	---------------------------	--------------	-----------------	-------------	-------------------------

\*\*APPLICANTS: Doi Yutaka; 3729

\*\*CONTINUING DATA VERIFIED: YES (me)  
THIS APPLICATION IS A DIV OF 09/752,408 12/28/2000 ALCATA

\*\* FOREIGN APPLICATIONS VERIFIED: None (me)

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ATTORNEY DOCKET NO
35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		H0001800 DIV (4960)
Verified and Acknowledged Examiners' initials (me)		
TITLE : Layered circuit boards and methods of production thereof		

U.S. DEPT. OF COMM. / PAT. & TM. PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
Amount Due		Sheets Drwg.	Figs. Drwg.
Date Paid		Print Fig.	
Primary Examiner		Application Examiner	
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	
WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.			

FILED WITH: ☐ DISK (CRF) ☐ CD-ROM  
(Attached in pocket on right inside flap)

BEST AVAILABLE COPY